

Figure 1A

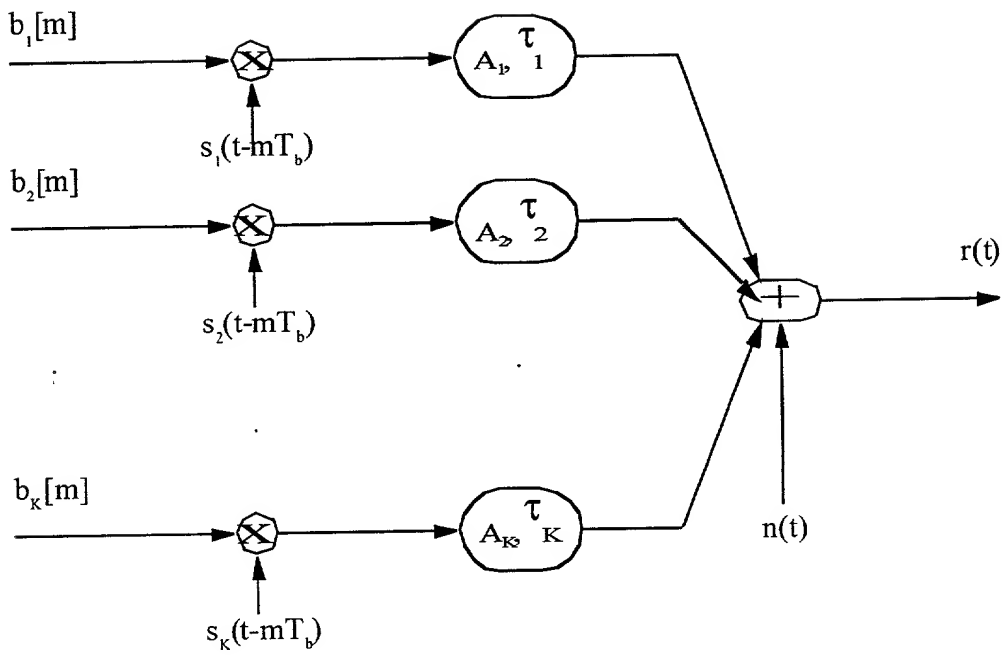


Figure 1

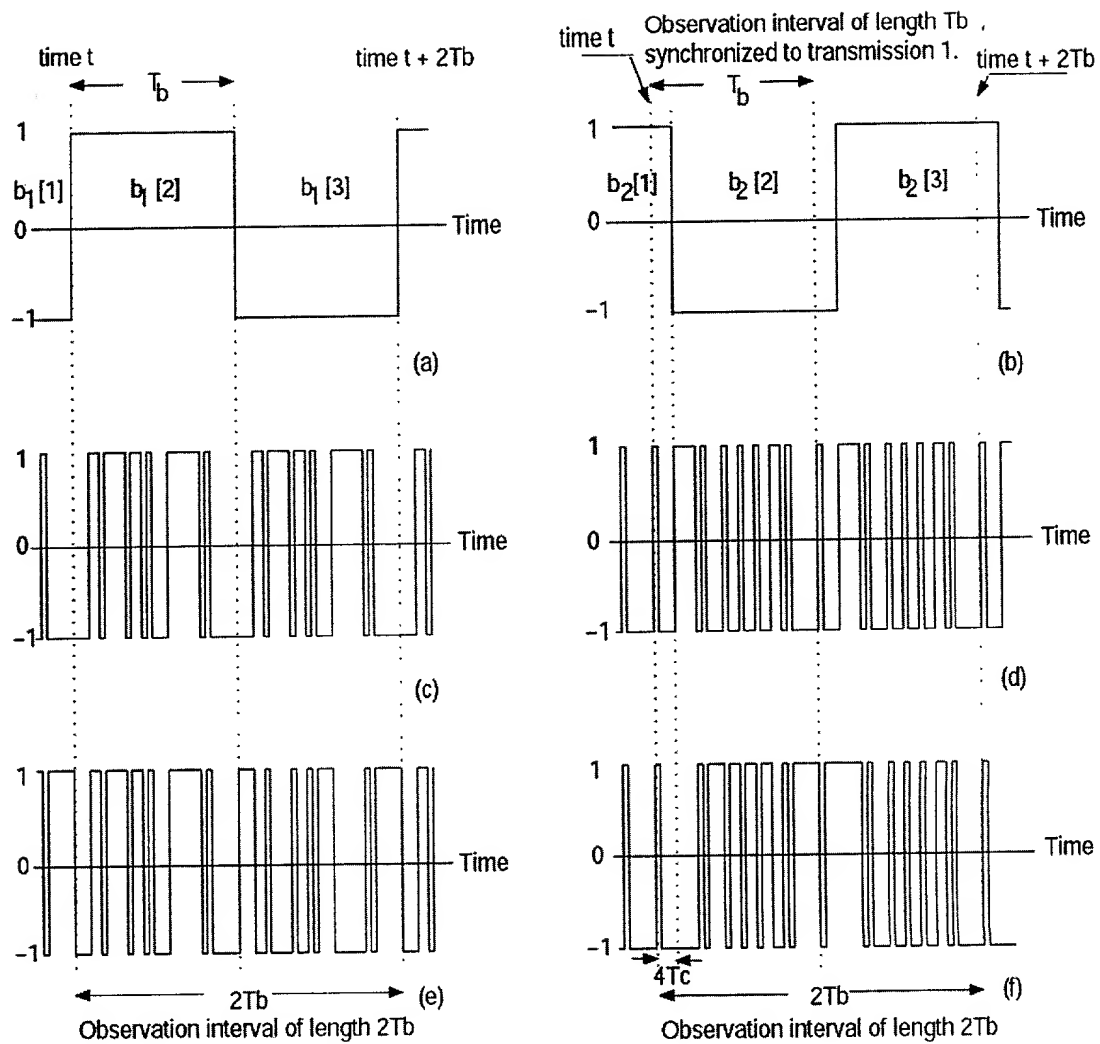


Figure 2a – 2f

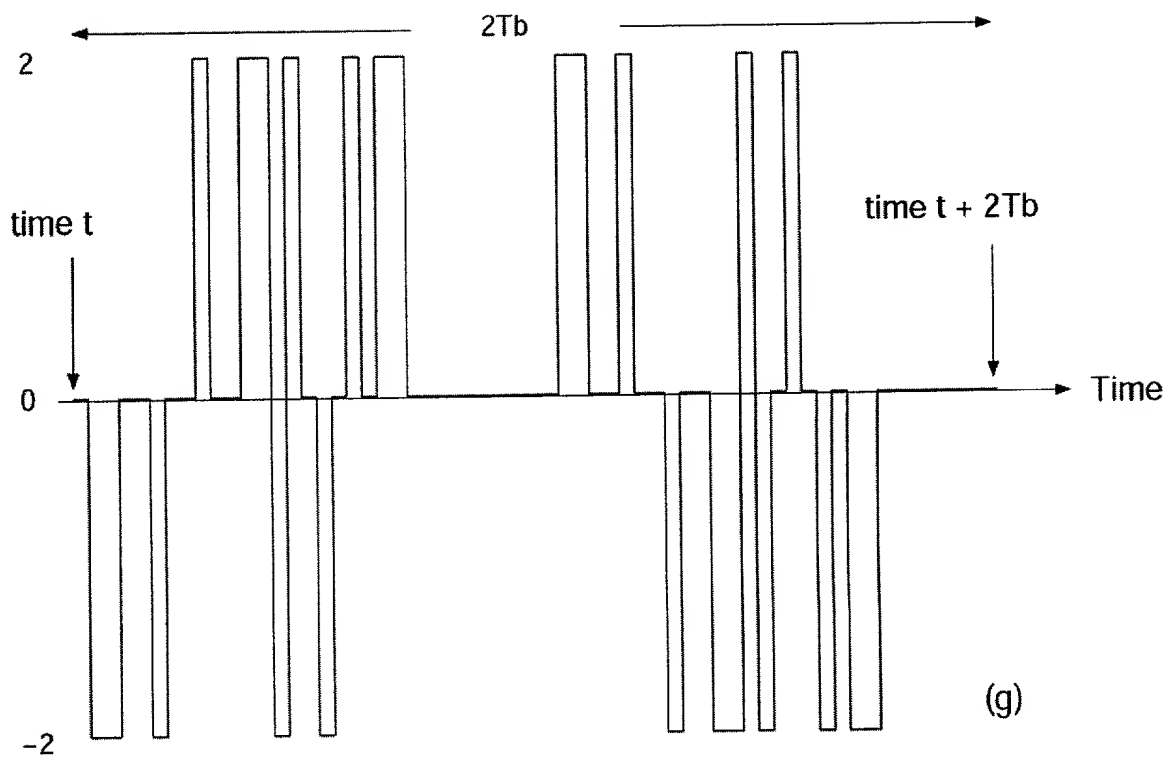


Figure 2g

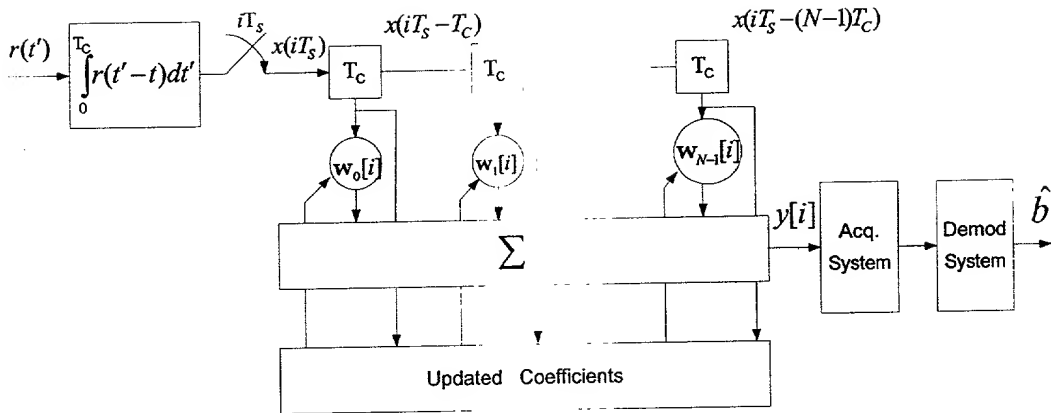


Figure 3

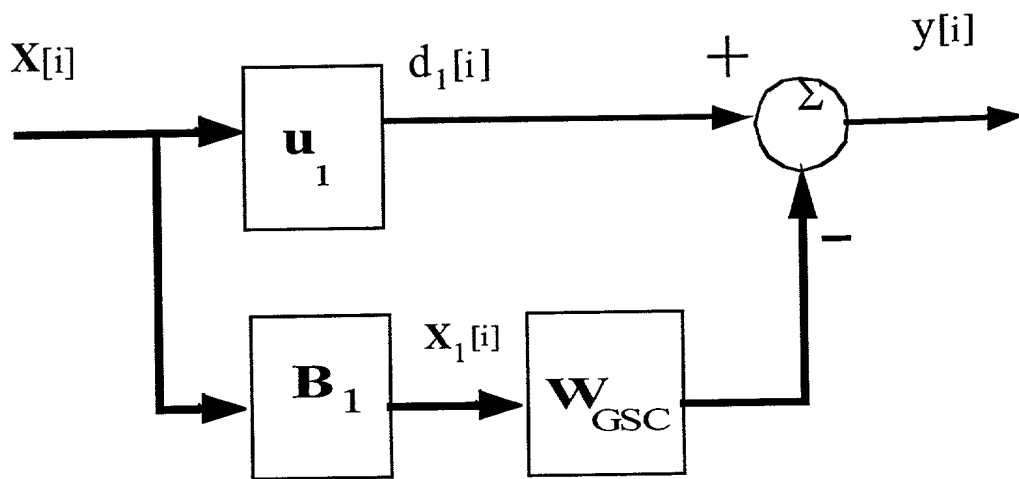


Figure 4

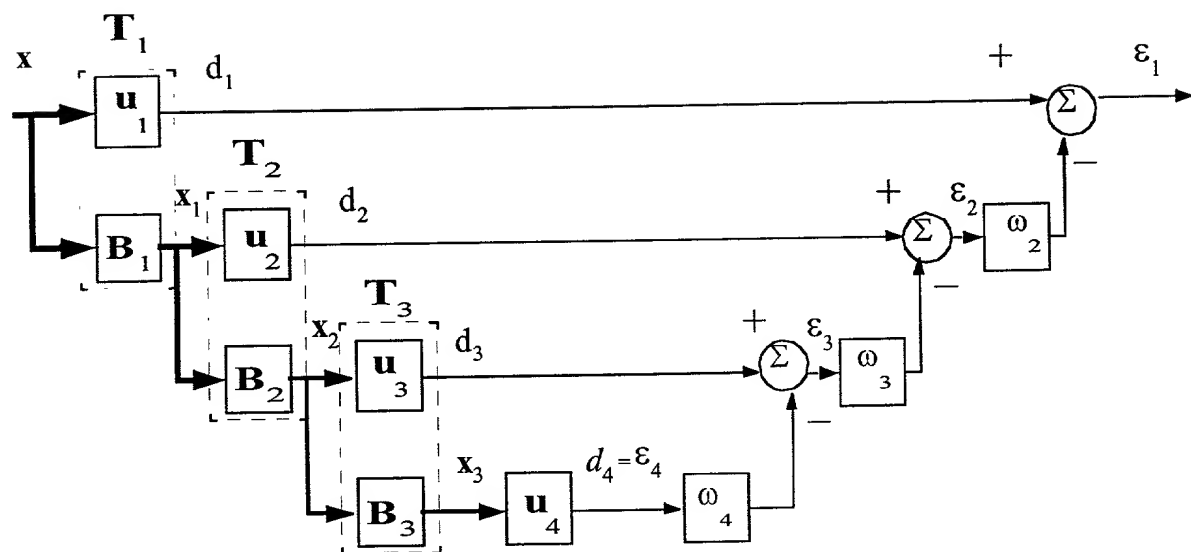


Figure 5

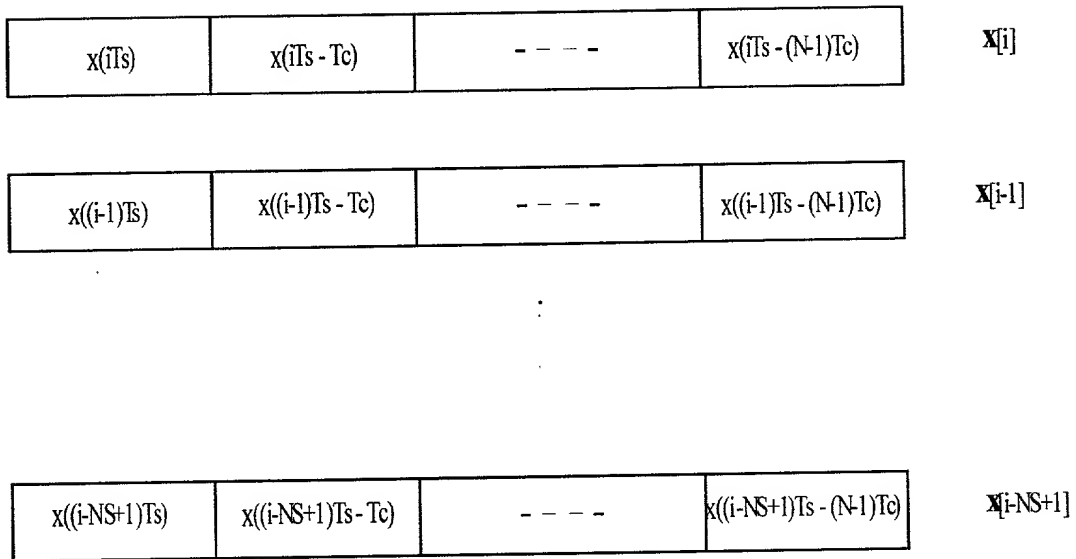


Figure 6

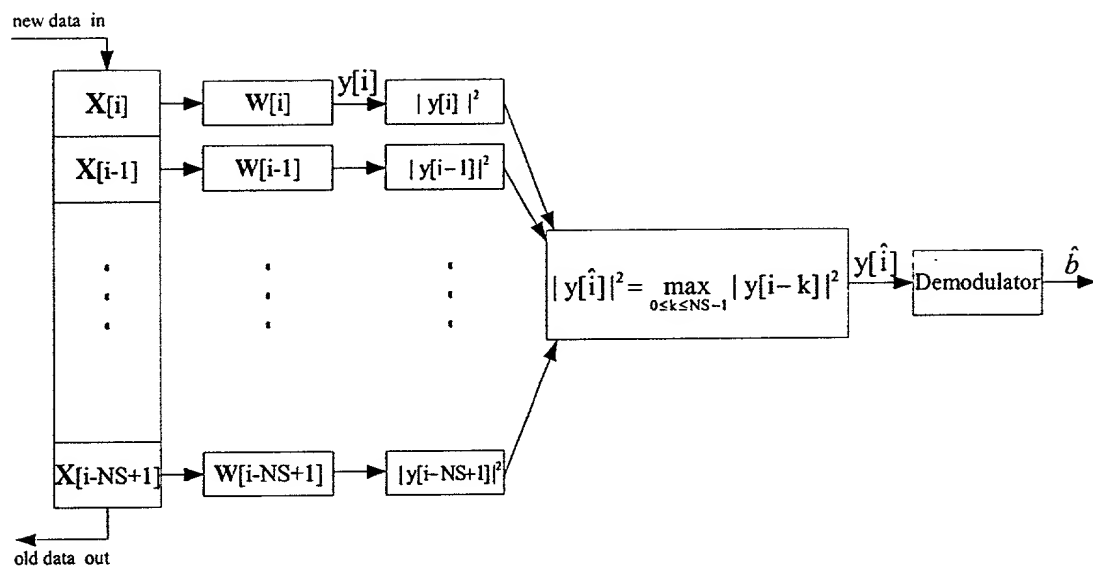


Figure 7: Block diagram of synchronization detector

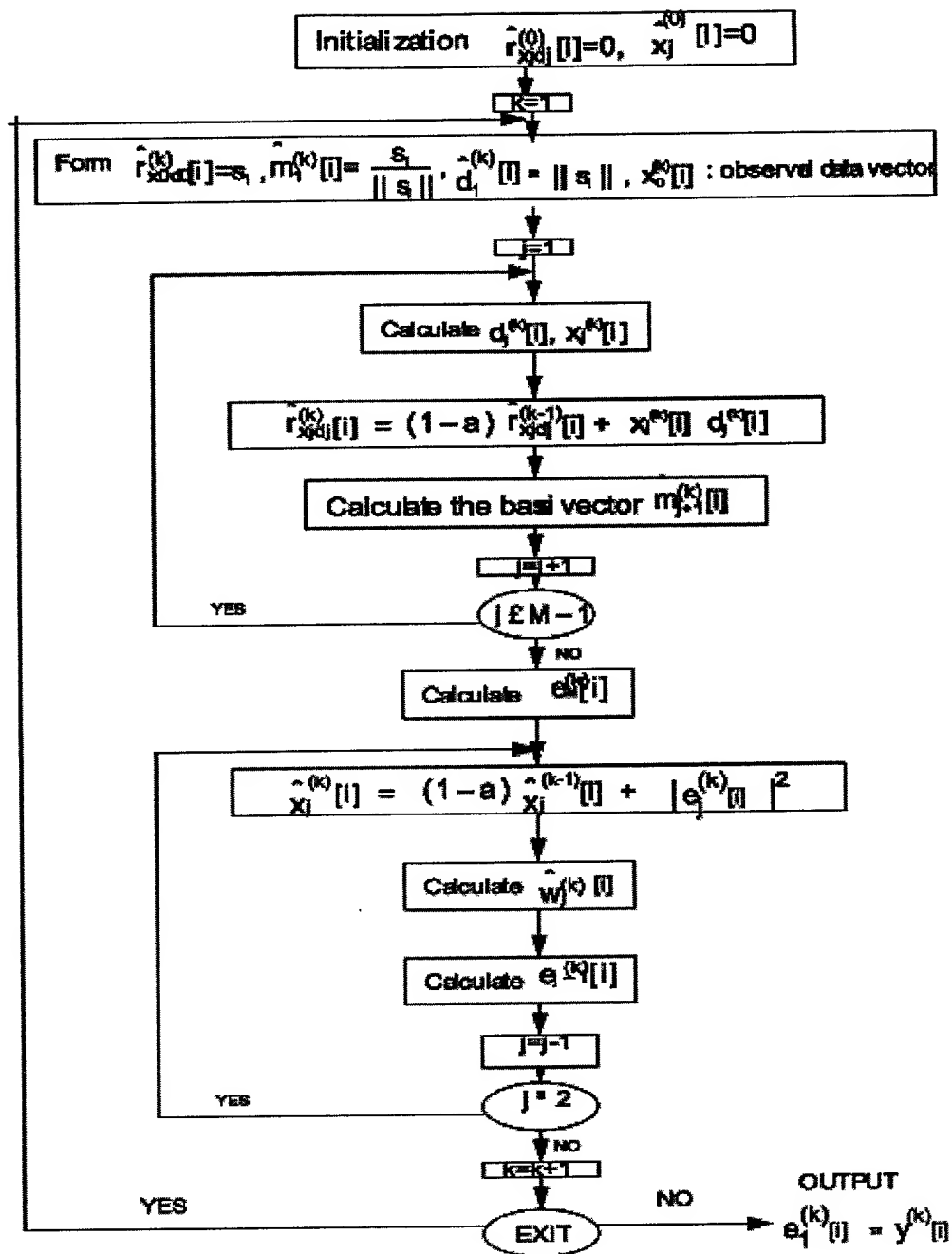


Figure 8

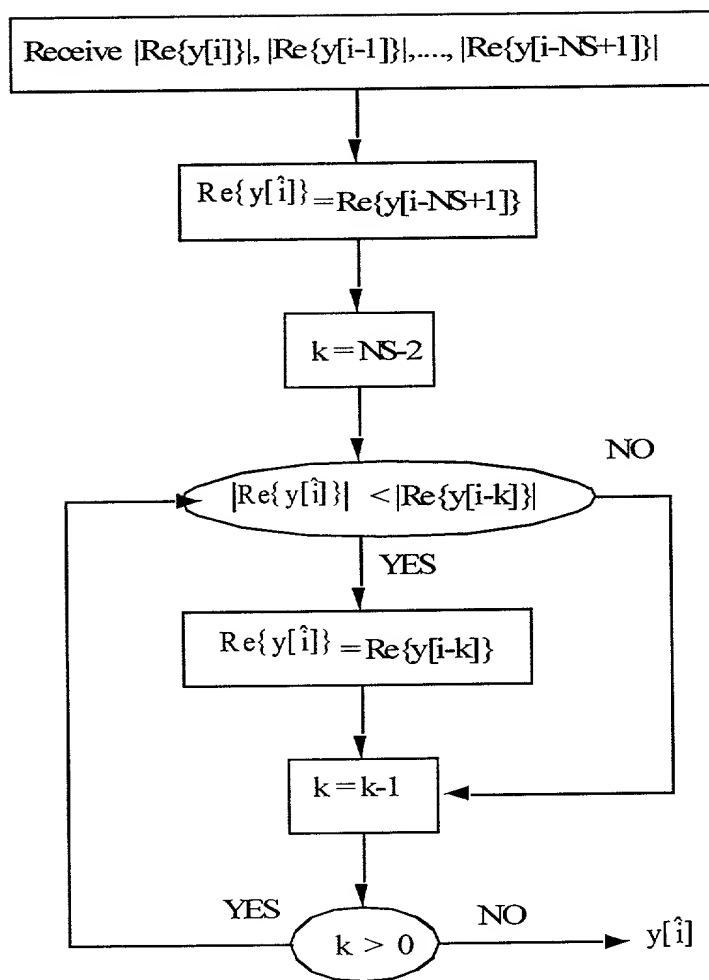


Figure 9

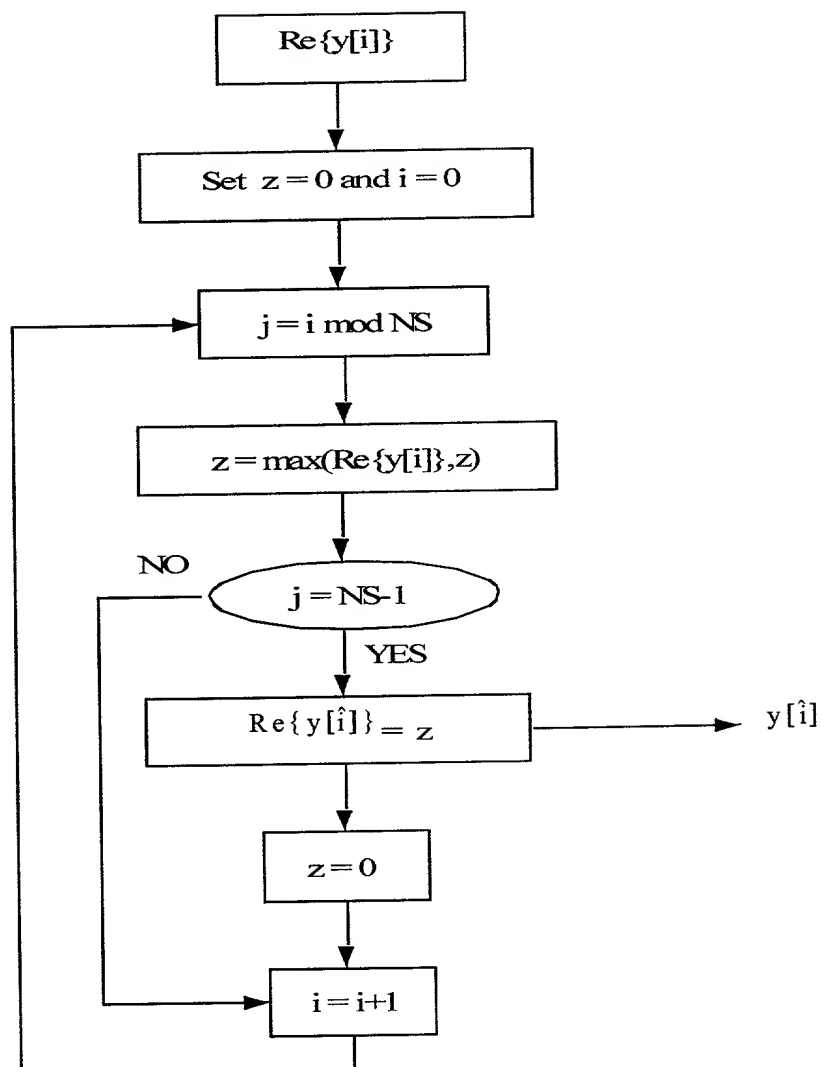


Figure 9A

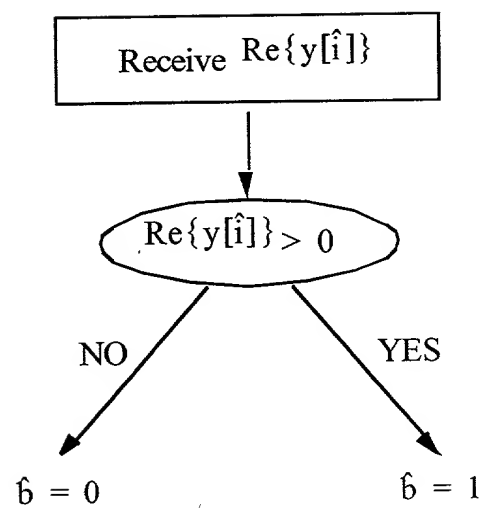


Figure 10

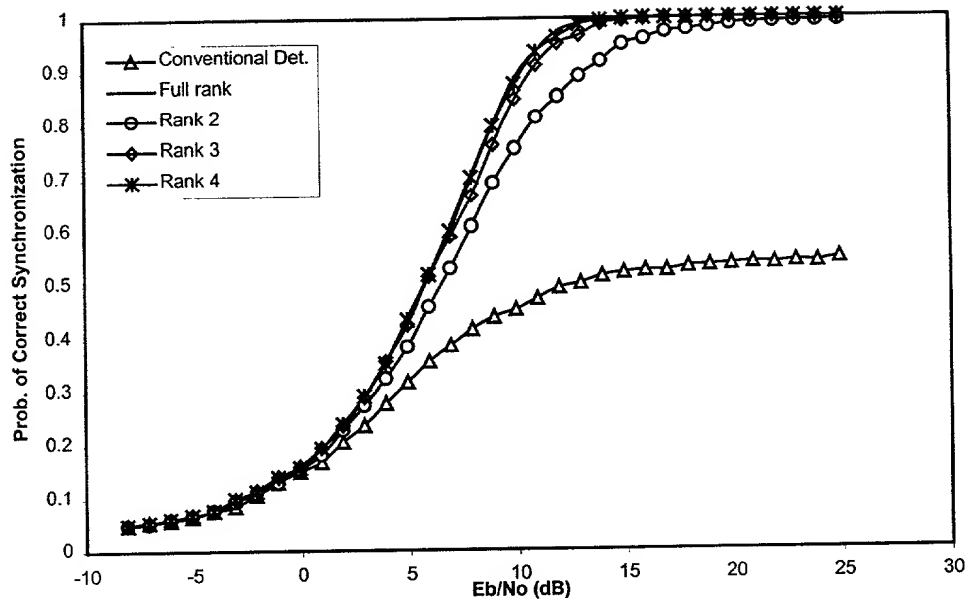


Figure 11a

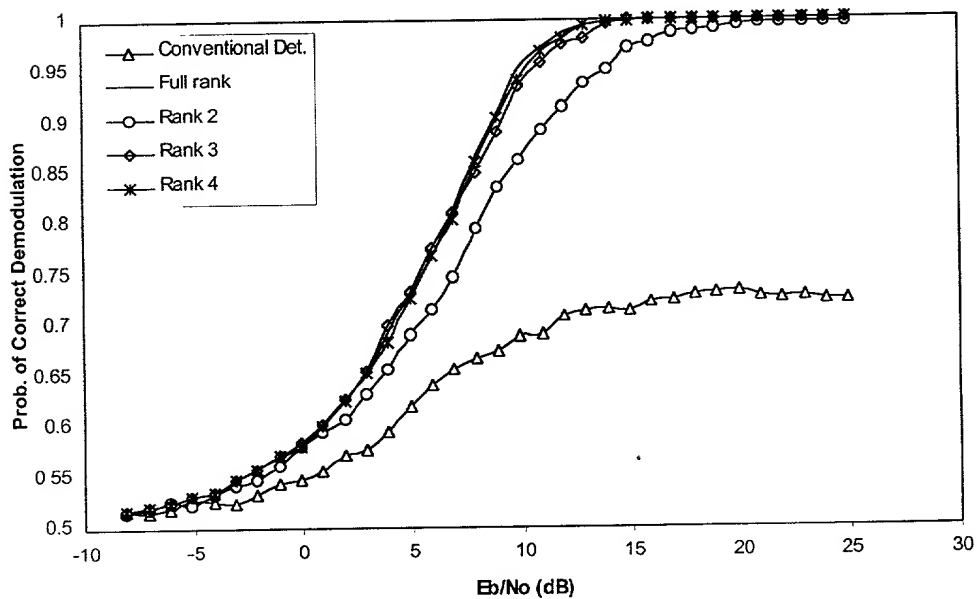


Figure 11b

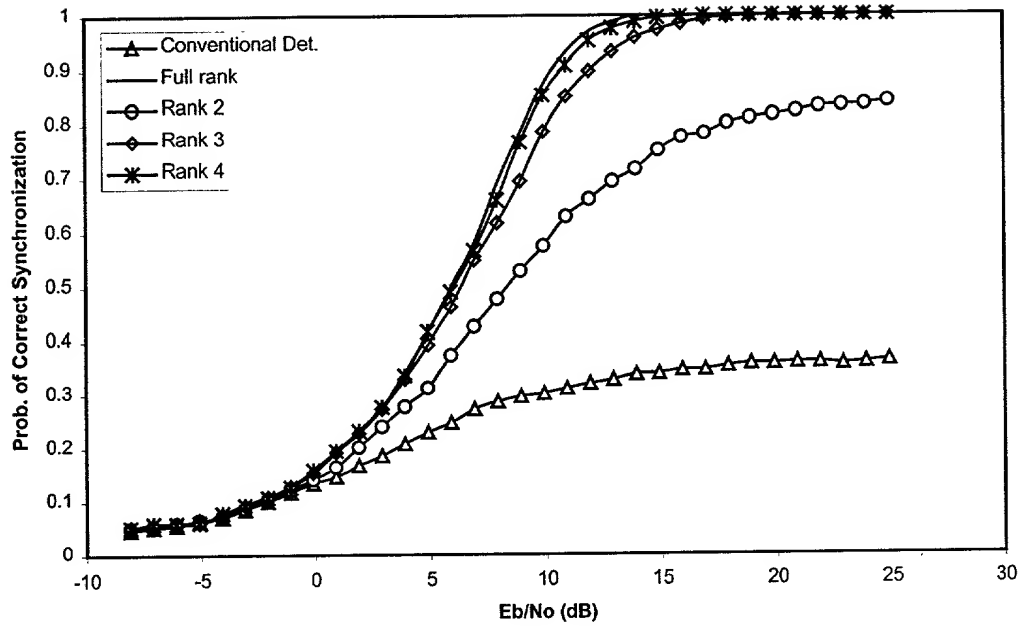


Figure 12a

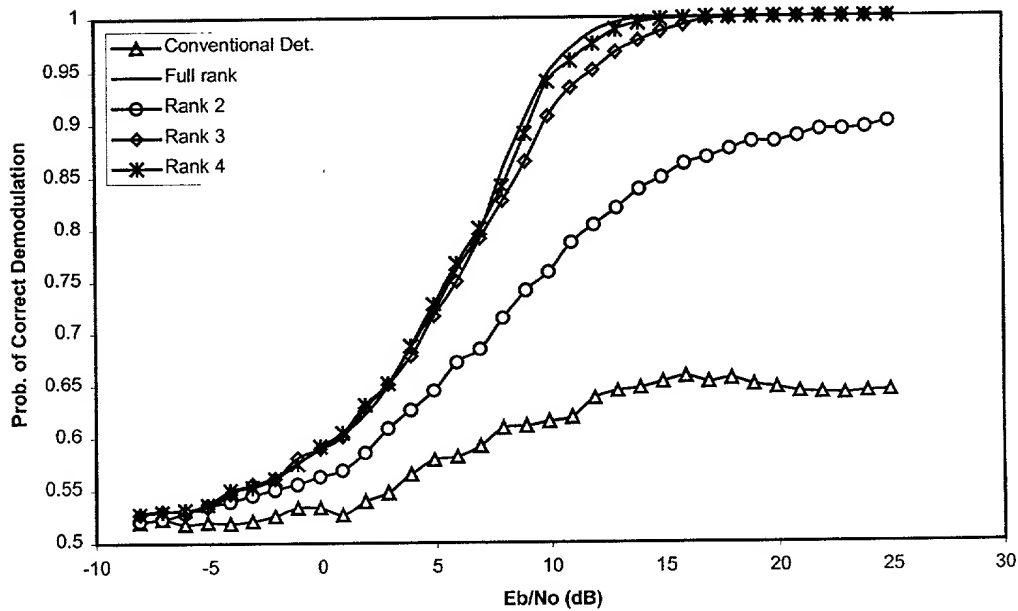


Figure 12b

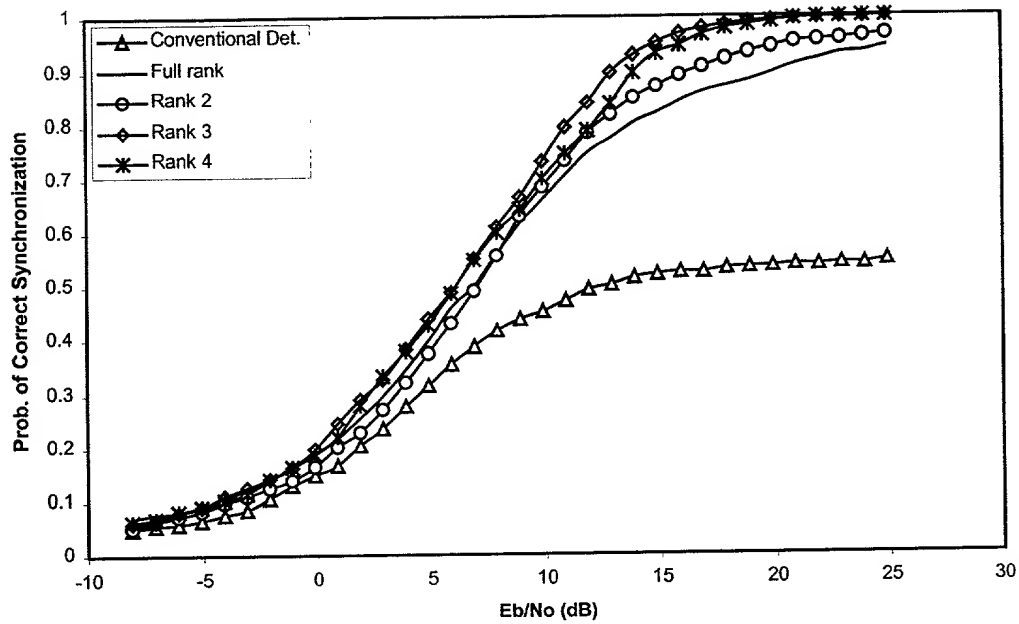


Figure 13a

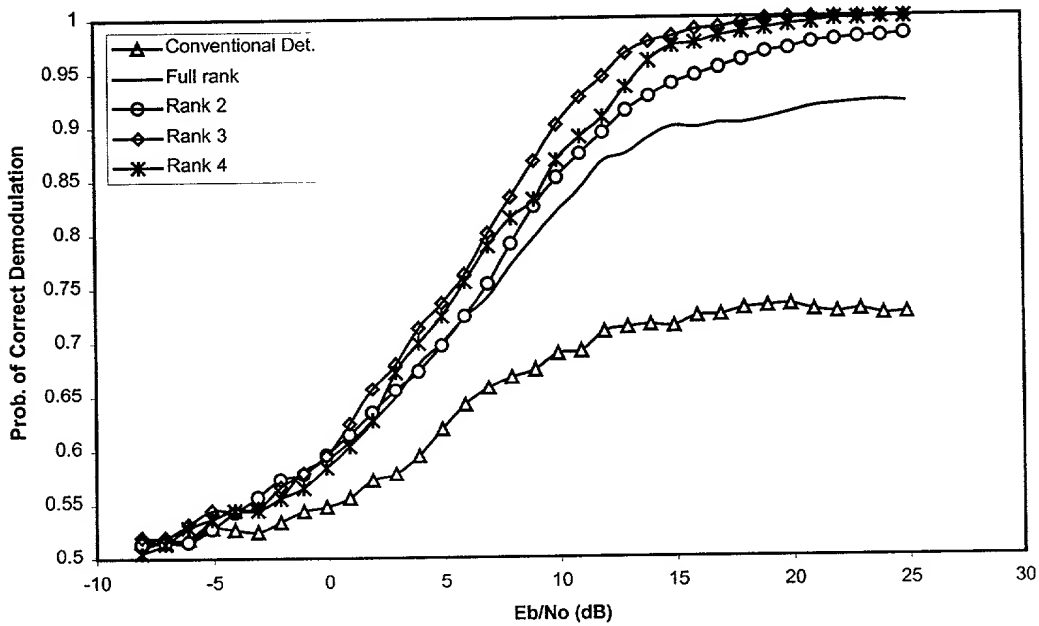


Figure 13b

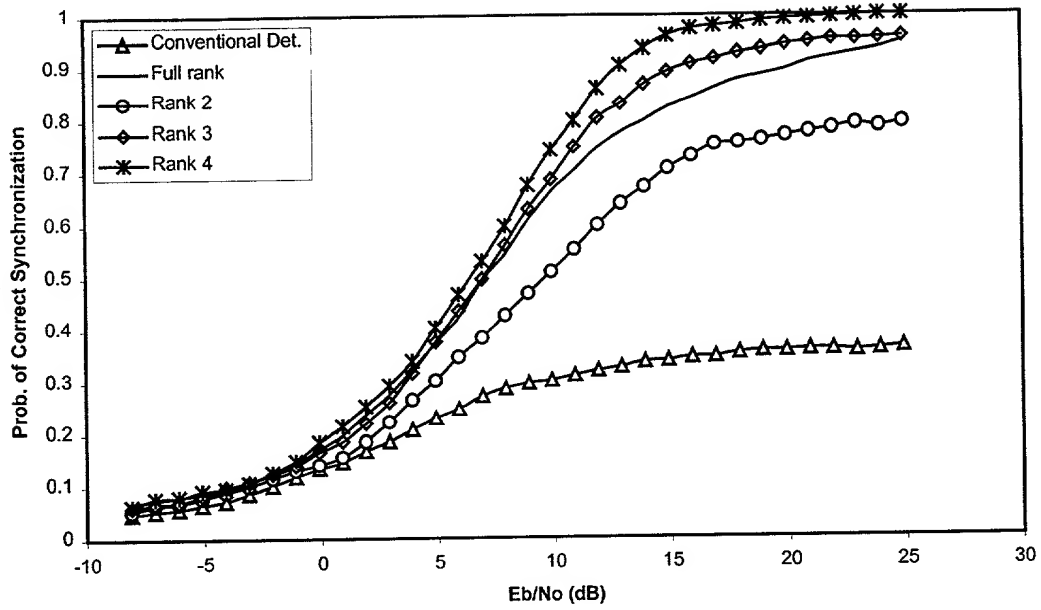


Figure 14a

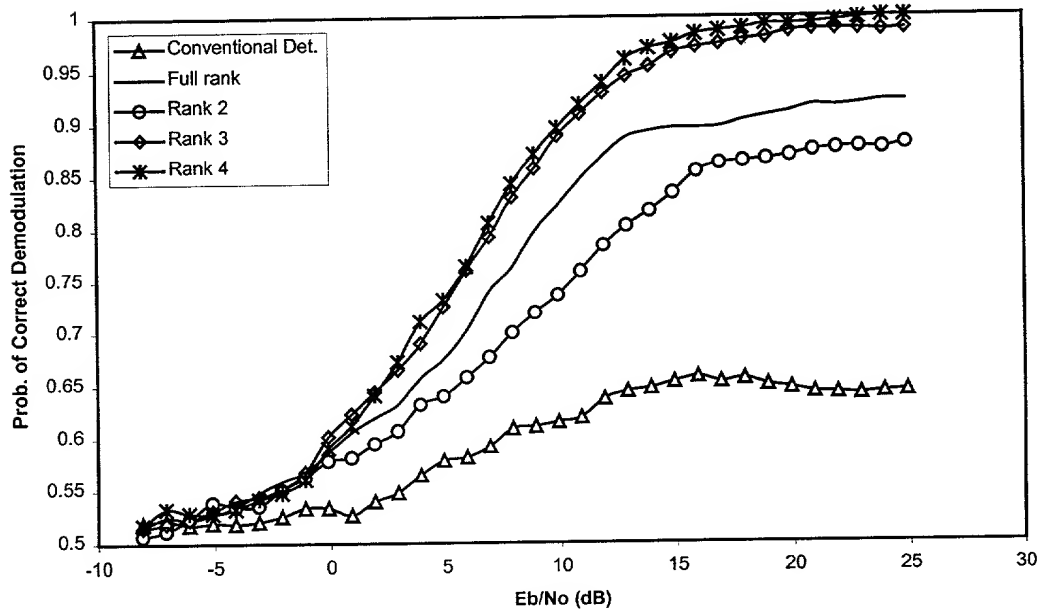


Figure 14b

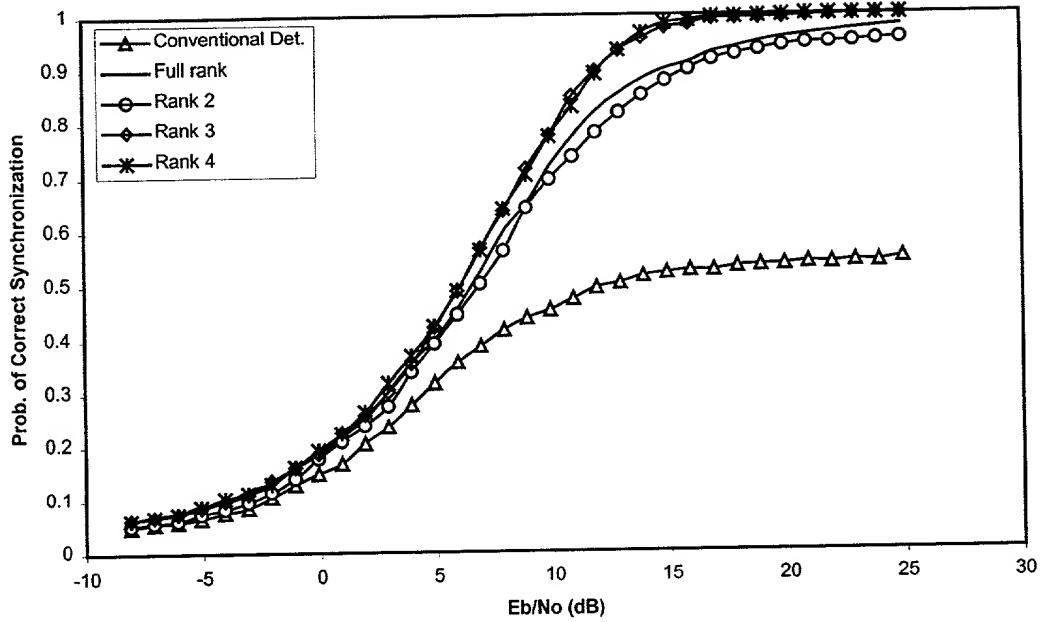


Figure 15a

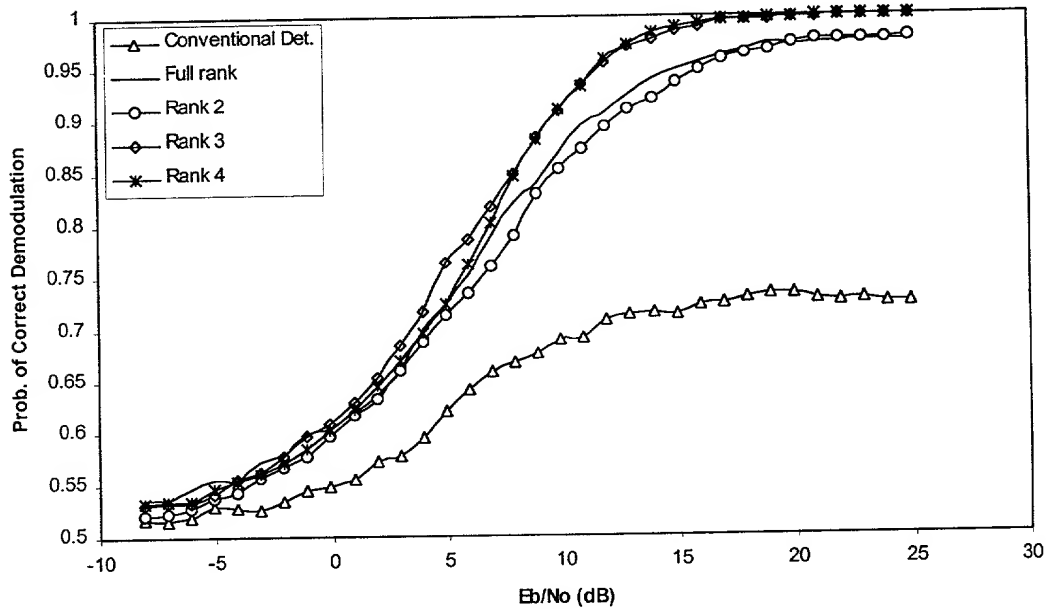


Figure 15b

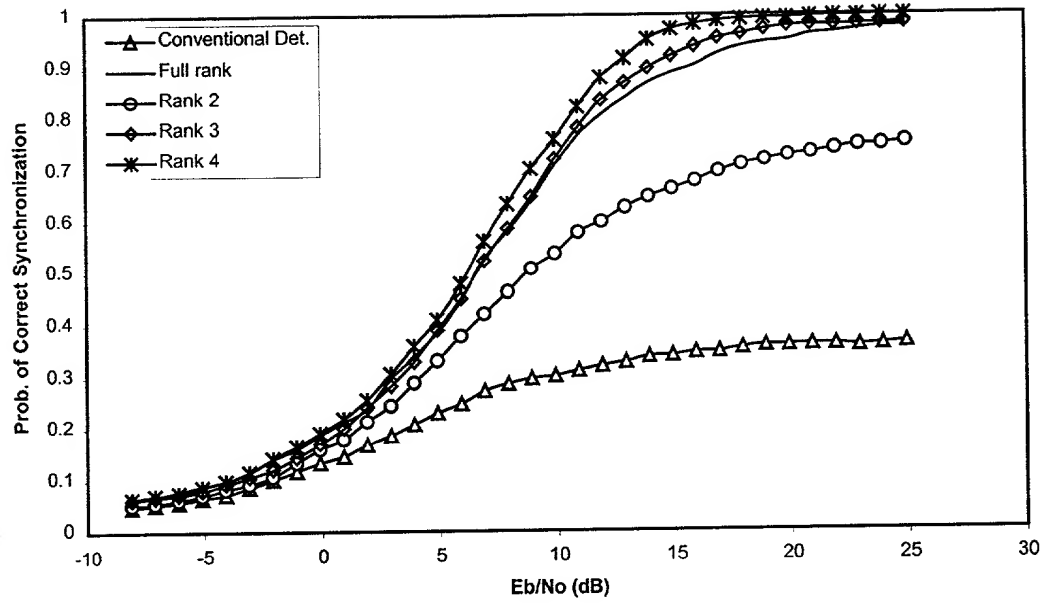


Figure 16a

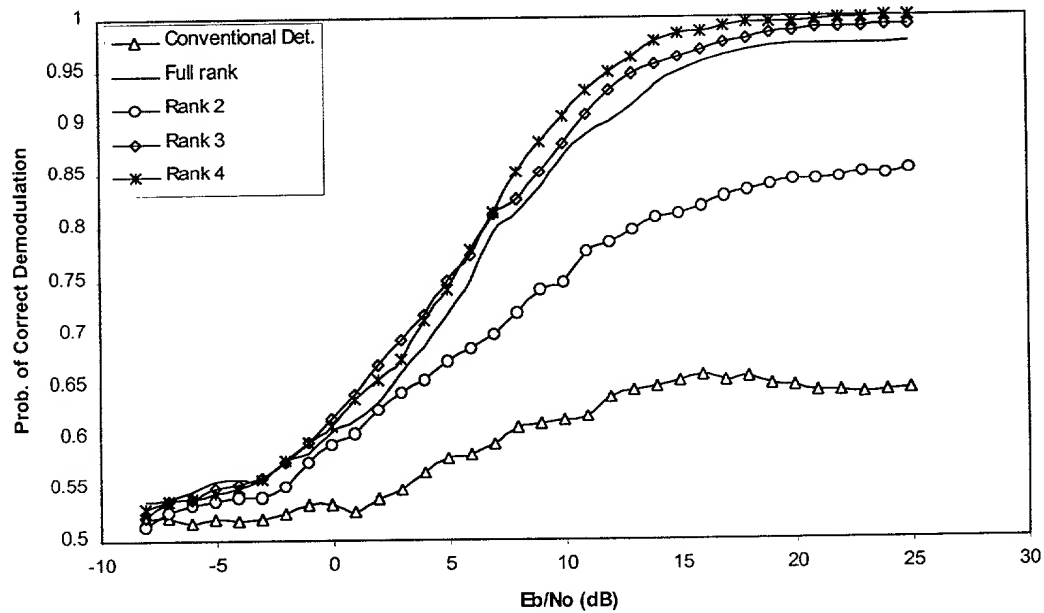


Figure 16b

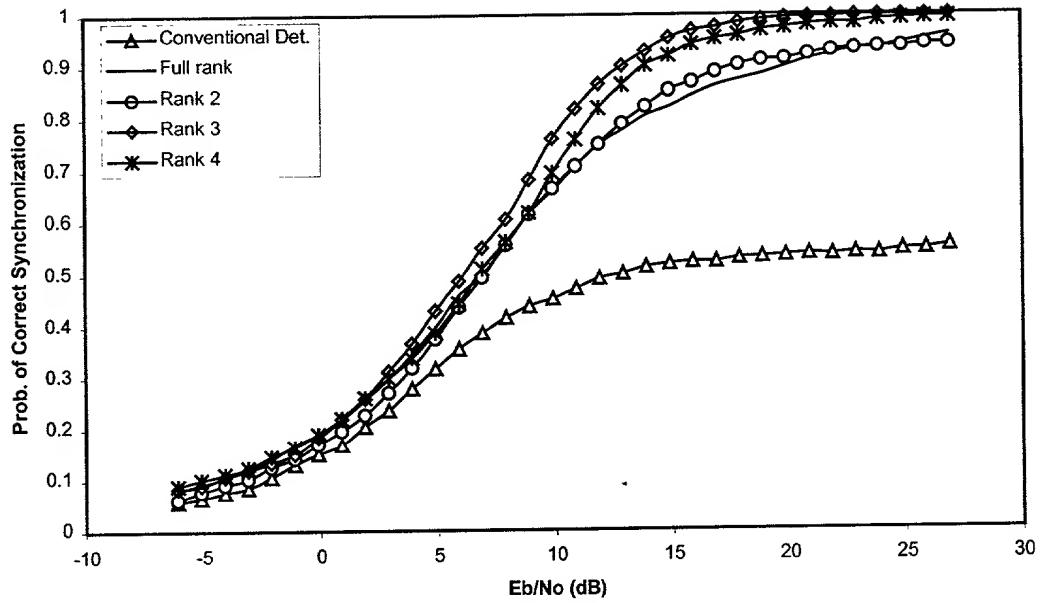


Figure 17a

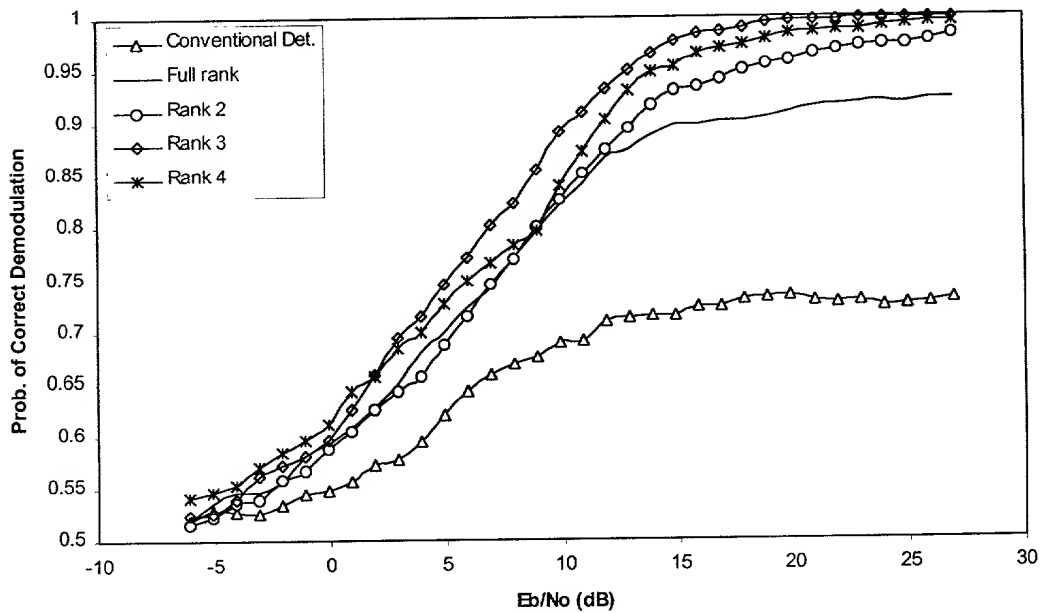


Figure 17b

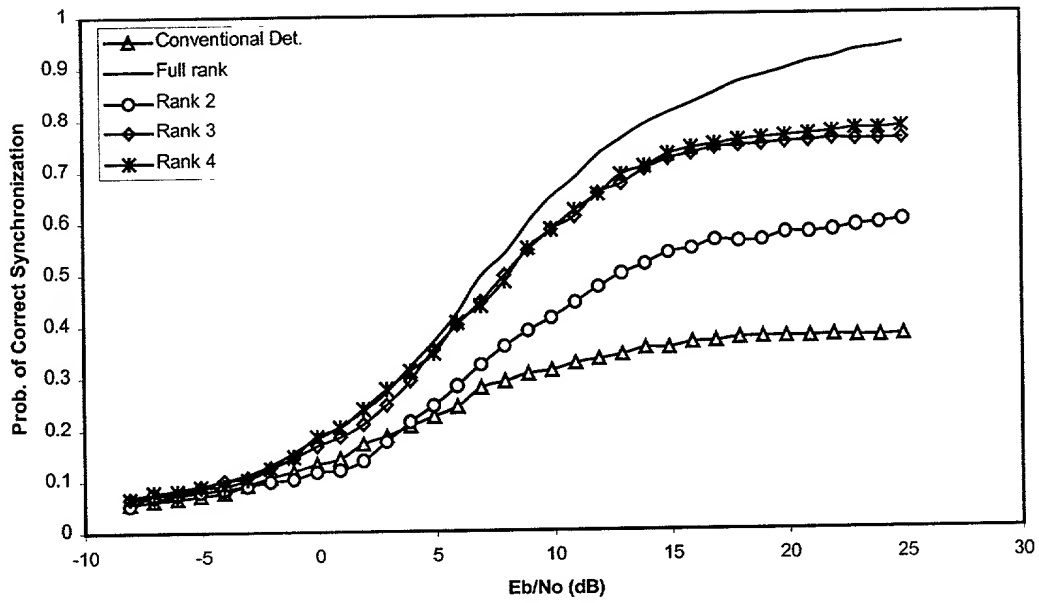


Figure 18a

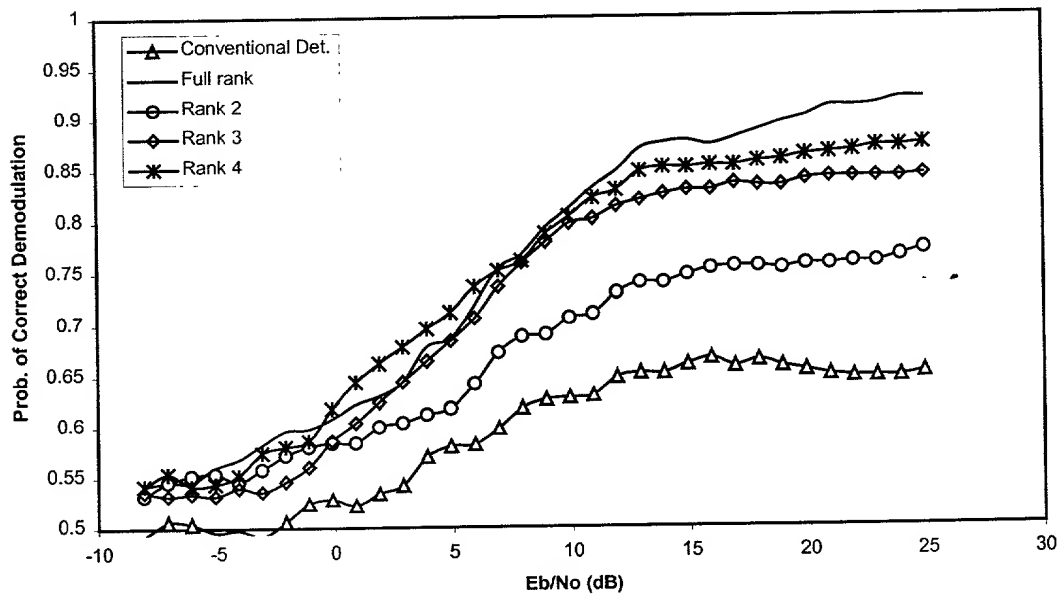


Figure 18b

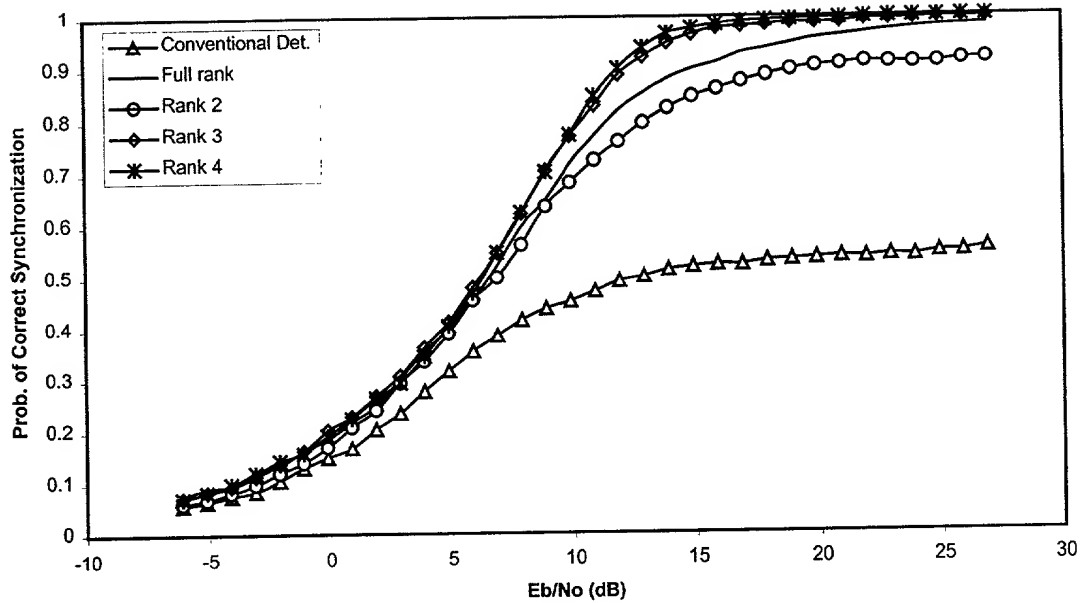


Figure 19a

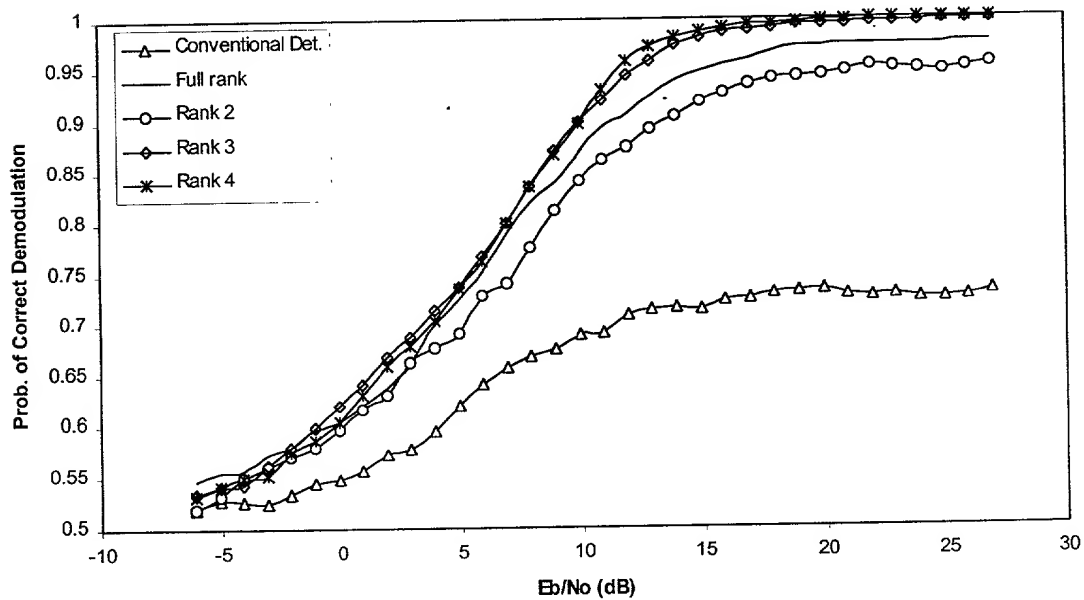


Figure 19b

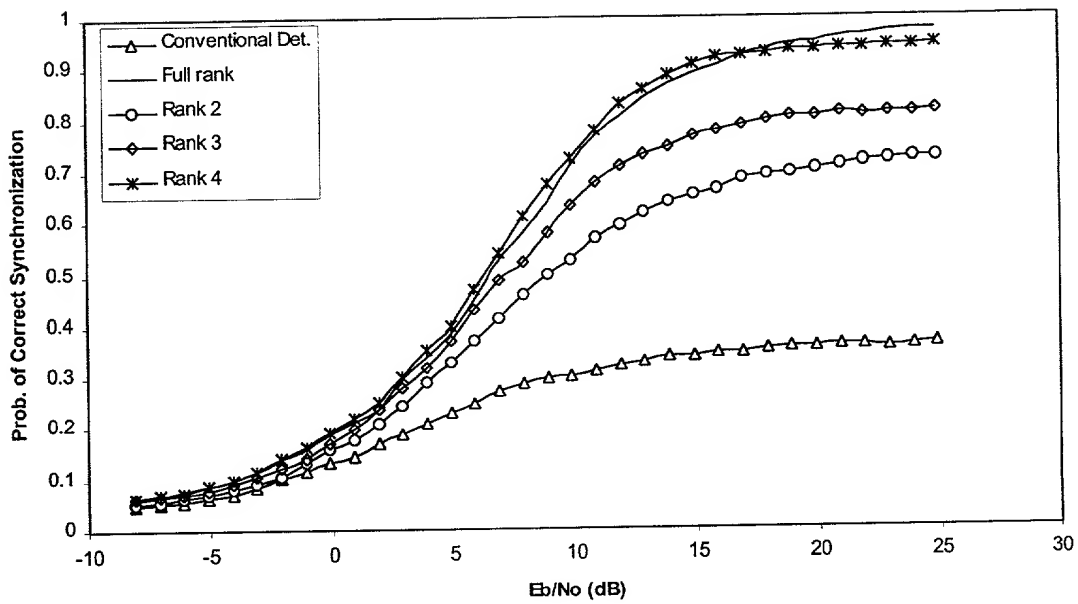


Figure 20a

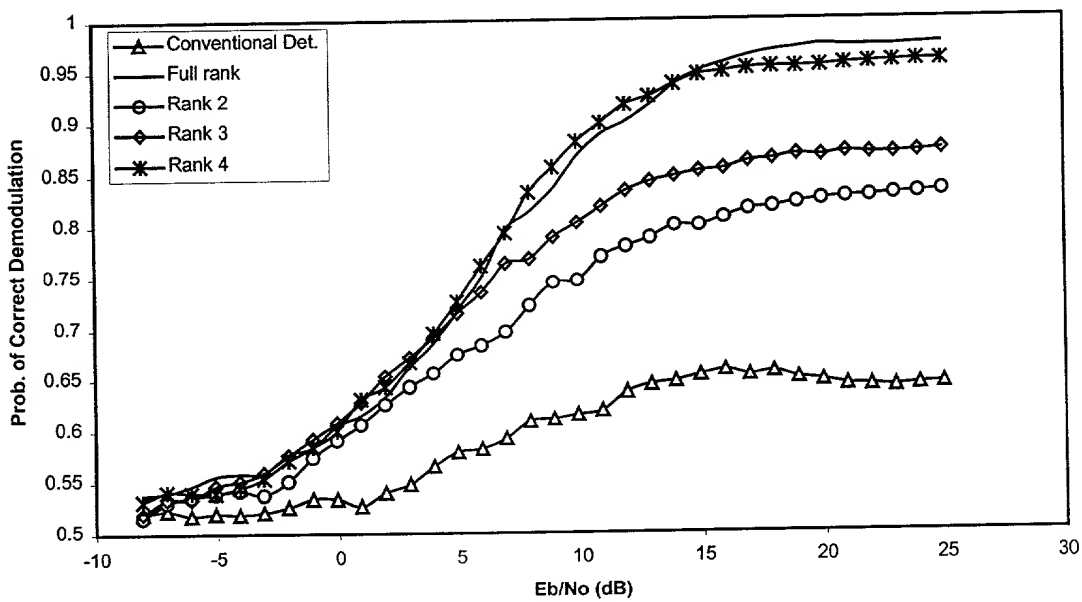


Figure 20b